

## Differences between DTC and DMAC(SH7145)

Item	DMAC	DTC
Number of Channels	4ch	More than 38ch
Setting of Transfer Data	Dedicated Register	Stored in the Memory (Mainly in the On-chip RAM) and Read at Every Activation
Transfer Size	Byte/Word/Longword	Byte/Word/Longword
Maximum transfer number of times	16M(16777216) Transfers	16M(16777216) Transfers
Transfer Mode	Cycle-Steal Mode Burst Mode	Normal Mode Repeat Mode Block Transfer Mode Chain Transfer Mode
Channel Function	Dual Address Mode Single Address Mode	Dual Address Mode Only
Channel Priority Ranking	Round Robin Mode	
Transfer Request	Auto Request (Activation by Software)	Activation by Software
	Interrupt Request from On-chip Peripheral Modules (MTU,A/D,SCI 10 Requests)	Interrupt Request from On-chip Peripheral Modules (Almost of All Built-In Peripheral Modules 38 Requests)
	External Request (2 DREQ Pins)	External Request (8 IRQ Pins)
Transfer Overhead	3 Cycles Minimum	9 Cycles Minimum

: Advantage points